

Curriculum Vitae – Jaco van de Pol – March 2011

1 General Information

Title : Prof. Dr.
Initials : J.C. (Jan Cornelis)
First name : Jaco
Surname : van de Pol
Date of birth : April 6, 1969
Place of birth : Barneveld
Nationality : Dutch

1.1 Affiliation

University of Twente phone : +31 (0)53 489 3017
Faculty of EEMCS mobile : +31 (0)53 489 6987
Research Institute CTIT secr : +31 (0)53 489 3767
Formal Methods and Tools fax : +31 (0)53 489 3247
P.O.-box 217
7500 AE Enschede e-mail : vdpol@cs.utwente.nl
The Netherlands URL : <http://fmt.cs.utwente.nl/~vdpol>

1.2 Home Address

A.M. de Jongstraat 18
7552 NL Hengelo (ov)
The Netherlands
Phone: +31 (0)74 7501502

2 Research Interests

Analysis of safety, dependability and security aspects of software-intensive embedded systems, by means of model checking, theorem proving and testing. More specifically, the development of new model checking techniques based on symbolic techniques (e.g. abstraction, confluence, fixpoint equation systems, constraint solving) and high-performance computing (e.g. distributed model checking, multi-core and out-of-core algorithms). Application domains include embedded systems, distributed systems, security protocols, and biological systems.

3 Education

- **Master degree:** December 1992
University : Utrecht University, Department of Computer Science
Main subject : Theoretical Computer Science (term rewriting)
Predicate : Cum laude
- **Doctoral degree:** December 1996
University : Utrecht University, Department of Philosophy
Supervisor : Prof. Dr. J.A. Bergstra
Title of thesis : Termination of Higher-order Rewrite Systems

4 Working Experience

- Dec 1992 – Oct 1996: PhD at Utrecht University, Department of Philosophy, Section of Theoretical Philosophy, Applied Logic.
- May 1994 – Jan 1995: Scientific assistant at the Ludwig Maximilian Universität München, Mathematisches Institut, section Mathematical Logic.
- Nov 1996 – May 1999: Postdoc at the Eindhoven University of Technology, Department of Computing Science, Section Technical Applications.
- May 1999 – Aug 2007: Senior researcher at the Centrum voor Wiskunde en Informatica, Amsterdam, Dept. of Software Engineering (SEN 2).
- May 2004 – Aug 2007: Associate Professor (UHD, 0.2 fte) at the Technische Universiteit Eindhoven, Subsection: *Design and Analysis of Systems* (OAS).
- Sept 2004 – Aug 2007: Theme Leader at the Centrum voor Wiskunde en Informatica, Amsterdam, Theme SEN 2: *Specification and Analysis of Embedded Systems*.
- Sept 2007 – : Full Professor, Faculty of EEMCS, CTIT, University of Twente: *Chair Formal Methods and Tools*

5 Academic Duties and Honours

- Coordinator 3TU.CeDICT-Twente (Centre on Dependable ICT Systems)
- Expertise council CTIT (Centre for Telematics and Information Technology)
- Chairman committee “Education 2030” Faculty EEMCS
- Programme Leader Twente Graduate School on *Dependable and Secure Computing*
- Chairman NVTI (Dutch Association of Theoretical Computer Science)
- Board Member IPA (Instituut voor Programmatuurkunde en Algoritmen)
- Vice-chair of the ERCIM working group FMICS
- Conference Program Chair SPIN 2010 on Model Checking Software
- Invited Speaker PDMC 2011 on Parallel and Distributed Model Checking

6 Teaching

- 1990–1992: Student assistant (Utrecht University/CS)
- 1995,1996: Lectures + Exercise classes Logic Programming (UU/CKI, 1st year)
- 1997: Exercise classes Language and Proof (TU/e, 1st year)
- 1998: PVS course (TU/e, OOTI, post-master students)
- 1998: Exercises classes Models of Computation (TU/e, 3rd year)
- Supervision of several internships and students at UU, TU/e, VU and CWI.
- 2005, 2006 (also planned in 2007): Lecturer of Master Course 2IW50: “Algorithms for Model Checking” (TU/e, 2nd year master)
- Basismodellen (BSc UT: 2008, 2009, 2010, 2011)
- Verification Engineering (Project course UT: 2010, 2011)
- Modelling and Analysis of Computer Systems 1 (MSc UT: 2008, 2009, 2010)
- Modelling and Analysis of Computer Systems 2 (MSc UT: 2009, 2010)

7 Research Projects

7.1 Project Leader/(co-)Applicant/Coordinator

- STW/Progress project. Applicant, project leader, 03/2000–03/2005
2 PhD students, supervision over 2 PhD students (Orzan, Valero).
On: Formal methods for Shared Dataspace Architectures.
Partners: Thales Nederland, 4TEC, CWI.
- NWO-EW project IT-VDS. Applicant, project leader, 10/2001–10/2005
2 PhD students, supervision over 1 PhD student (Badban).
On: Integrating Techniques for the Verification of Distributed Systems
Partners: TU/e, CWI.
- NWO-EW project MoveBP. Co-applicant. 01/2004-01/2008.
On: Modeling and Verification of Business Processes
Partners: CWI, TU/e.
- NWO-Focus project VeriGEM. Co-applicant. 11/2005-11/2008
On: Verification Grid for Enhanced Model Checking.
Partners: CWI, TU/e, UT.
- NWO-EW project VEMPS. Co-applicant. 09/2006-08/2010
On: Multi-party security protocol analysis with Process Theory and Epistemic Logic.
Partners: CWI, TU/e.
- EU FP6-NEST-PATH: EC-MOAN. Project Coordinator, 2/2007-2/2010
On: “Scalable Modeling and Analysis Techniques to Study Emergent Cell-behaviour,
Understanding the *E. Coli* stress response”.
Partners: CWI, Inria, UJF Grenoble, MU Brno, VU Amsterdam, Un. of Edinburgh.

- EU FP7-TRANSPORT: INESS. Co-applicant. 10/2008-10/2011, 1 PD.
On: Integrated European Railway Signalling System.
- NWO-EW project Syrup. Co-applicant. 03/2009-03/2013, 1 PhD
On: Symbolic Reduction of Probabilistic Models.
- NWO-EW project VOCHS. Co-applicant. 09/2010-09/2014
On: Verification of complex hierarchical systems
Partners: TU/e, UT. 2 PhD students. Supervising 1 PhD student (Kant).

7.2 Project Member

- EU Science Twinning Contract. Scientific assistant, 05/1994–01/1995.
On: (termination) proofs and computation.
Partners: LMU Munich, Leeds University, Oslo University.
- Senter-project ORKEST. Postdoc, 11/1996–05/1999.
On: formal requirements specification of command and control systems.
Partners: Hollandse Signaalapparaten bv (coordinator), TU/e, RUG, UvA, CWI.
- Philips Natlab internal project. Project member, 05/2000–12/2000.
On: automated test generation for MPEG-audio decoders.
Partners: Philips Natlab, CWI.
- Systems Validation Center (SVC). Project member, 09/1999–12/2002.
On: model checking and theorem proving for Telecom systems.
Partners: Telematica Institute, UT, CMG, IBM, KPN, Lucent, CWI.
- KTV-project (Min. of Defense). staff member, 02/2002–04/2002.
On: formal methods for a data-acquisition unit in a LYNX helicopter.
Partners: Dutch Royal Navy (funding), NLR, CWI.
- NWO-EW project TIPSy. Staff member, 07/2003-07/2007.
On: tools for performance analysis and system verification.
Partners: TU/e (Departments of CS and ME), ASML, CWI.
- NWO-EW project ACCOUNT. Staff member, 01/2004-01/2008.
On: accountability in electronic commerce protocols.
Partners: CWI, UT, VU.
- European ITEA-project TT-medal; ITEA best-achievement Award-winner 1995.
Staff member, 01/2004–01/2005.
On: automatic TTCN-3 test generation from UML 2.0 models.
Partners: CMG, ProRail, Improve QS, CWI, Fraunhofer/Fokus, Testing Technologies, DaimlerChrysler, Nokia, VTT, Conformiq, Nethawk.
- BSIK-BRICKS: Basic Research in Informatics for Creating the Knowledge Society.
On: Parallel and Distributed Computing, Algorithms and Formal Methods.
Partners: CWI, NWO, TU-Delft, TU/e, U Twente, U Utrecht.

- SENVA: International Joint Research Team on Safety-Critical Systems
Partners: CWI (SEN2), INRIA (Vasy).

8 Other International Activities

- Lecturer at Types Summerschool, Giens, France, 09/1999
- Participant at Dagstuhl workshop “Proof Theory in Computer Science”, 10/2001.
- Vice-chair of the ERCIM working group FMICS, “Formal methods for Industrial Critical Systems”
- Reviewer of European ITEA-project ‘Sophocles’ (2002,2003).
- Research visit to AIST-Amagasaki, Japan, 03/2003 (two weeks).
- Co-organizer SENVA workshops 06/2004, 06/2005, 11/2005, 04/2006, 06/2006.
- Research visit to Masaryk University, Brno, 12/2006 (two weeks).
- Participant at Lorentz Center Workshop “Two Decades of Probabilistic Verification”, 11/2007.
- Participant at Dagstuhl “Distributed Verification and Grid Computing”, 08/2008.
- Keynote Speaker at HIBI (High-performance computing in Biology), Trento (2009)
- Teacher Tutorial “Problem solving with Model checking techniques”, ICAPS, 06/2011.
- Invited Speaker on PDMC 2011, Snowbird, Utah, USA.
- PC-member of International Conferences:
 - TACAS, Tools and Algorithms for the Construction and Analysis of Systems (2004, 2006)
 - AMAST, Algebraic Methodology And Software Technology (2004, 2006)
 - IFM, Integrated Formal Methods (2005, 2007)
 - FM 2009, Formal Methods Europe
 - SOFSEM 2011, Current Trends in Theory and Practice of Computer Science
- PC-member of International Workshops:
 - EXPRESS 2004, Expressiveness in Concurrency
 - WRS, Reduction Strategies in Rewriting and Programming (’04,’06,’07, 2011)
 - PDMC, Parallel and Distributed Methods in Verification (’05, ’06, ’07, ’08, ’09, 2010, 2011)
 - FMICS, Formal Methods in Industrial Critical Systems (2007, 2009, 2010, 2011)

- FOCLASA, Found. of Coordination Languages and Software Architectures (2010, 2011)
- HIBI, High-performance Computational Systems Biology (2009, 2010)
- SPIN 2010, Model Checking Software, Twente University.

- Co-chair of International Conferences and Workshops
 - PDMC 2005, IW on Parallel and Distributed Methods in Verification
 - IFM 2005, IC on Integrated Formal Methods, Eindhoven
 - PDMC 2006, IW on Parallel and Distributed Methods in Verification
 - PDMC 2009, IW on Parallel and Distributed Methods in Verification
 - SPIN 2010, IW on Model Checking Software, Twente.
- Reviewer for journals:
 - Communications of the ACM
 - Engineering Applications of Artificial Intelligence
 - Theoretical Computer Science
 - Transactions on Software Engineering
 - Science of Computer Programming
 - Software Tools and Technology Transfer
 - IEEE/ACM Transactions on Computational Biology and Bioinformatics

9 Other National Activities

- Secretary NVTI (Nederlandse Vereniging voor Theoretische Informatica)
- Chairman NVTI
- Board Member IPA (Instituut voor Programmatuurkunde en Algoritmen)
- (Co-)organized national events:
 - 6th Dutch Testing Day, 11/2000, CWI Amsterdam.
 - SAFE-NL, the Dutch security workshop, 06/2002, at CWI Amsterdam.
 - Dutch Proof Tools Day, 04/2003, CWI, Amsterdam.
 - Symposium on Embedded Software Quality, Aug 2005, CWI, Amsterdam.
 - Symposium Processes, terms, and cycles: steps on the road to infinity.
In honour of Jan Willem Klop, 12/2005, CWI, Amsterdam.
 - IPA Spring Days on Testing (program committee), Vught, 04/2006.
 - 11th, 12th, 13th, 14th NVTI Theory Days, Utrecht, 03/2005, 03/2006, 03/2007
 - IPA Course on Formal Methods (program chair), Eindhoven, 06/2008.
 - CTIT Symposium, *Dependable ICT - who cares?*, Twente, 06/2010.
- KiviNiria RADKNIT evening lecture, Utrecht, 12 april 2005.

- Co-promotor PhD defense:
 - Simona Orzan (VU, 25/11/2004)
 - Miguel Valero (VU, 5/12/2005)
 - Bahareh Badban (VU, 7/9/2006)
- promotor PhD defense:
 - Anton Wijs (VU, 2/10/2007)
 - Mohammed Dashti (VU, 27/2/2008)
 - Jens Calamé (UT, 4/9/2008)
 - Taolue Chen (VU, 21/09/2009)
- Reading Committee and/or Opposition PhD defense:
 - D. Hendriks, 31/10/2003, UU
 - J. Pang, 26/10/2004, VU
 - N. Ioustinova, 04/11/2004, VU
 - O. Tveretina, 29/6/2005, TU/e
 - N. Trcka, 28/06/2007, TU/e
 - A. de Groot, 06/03/2008, RU
 - I. Zapreev, 07/03/2008, UT
 - G. Gulesir, 13/03/2008, UT
 - H. Svensson, 21/04/2008, Chalmers U. of Technology, Sweden (opponent)
 - M. van Otterlo, 30/05/2008, UT
 - E. Bortnik, 01/07/2008, TU/e
 - H. Kastenbergh, 03/10/2008, UT
 - M. van Weerdenburg, 01/04/2009, TU/e
 - M. Wiggers, 19/06/2009, UT
 - T. Han, 25/09/2009, UT
 - S. Ciraci, 17/12/2009, UT
 - M. Neuhaußer, 22/01/2010, UT
 - P. Holzenspies, 23/04/2010, UT
 - P. Monteiro, 17/05/2010, Technical U. of Lisbon, Portugal
 - T. Staijen, 03/06/2010, UT
 - Y. Wang, 21/09/2010, UvA
 - R. Abdel Kader, 25/11/2010, UT

10 List of publications

10.1 Theses

1. J.C. van de Pol, *Modularity in many-sorted term rewriting systems*. Master's thesis, Utrecht (Informatica), 1992, 37 pages.
2. J.C. van de Pol, *Termination of higher-order rewrite systems*. PhD thesis, Utrecht (Philosophy), 1996, 160 pages.

10.2 Edited volumes

1. J.C. van de Pol, editor, Special Issue for the FMICS'02,'03 workshops. *Software Tools for Technology Transfer* 5(2-3): 105–106, 2004.
2. T. Arts and J.C. van de Pol, editors, Special Issue for the FMICS'04 workshop. *Software Tools for Technology Transfer*, 7(3):195–196, 2005.
3. J.M.T. Romijn, G. Smith, and J.C. van de Pol, editors. *Integrated Formal Methods, 5th Int. Conf., IFM 2005, Eindhoven, Proceedings*, LNCS 3771, Springer, 2005.
4. M. Leucker, J.C. van de Pol, editors, Proceedings of PDMC'05, *Electronic Notes in Theoretical Computer Science* 135(2):1-2, 2006
5. J.M.T. Romijn, G. Smith, and J.C. van de Pol, editors, Special Issue IFM Doctoral Symposium 2005, *Electronic Notes in Theoretical Computer Science* 191:1-2, 2007
6. L. Brim, B. Haverkort, M. Leucker, J.C. van de Pol, editors, *Formal Methods: Applications and Technology*, Proc. of FMICS+PDMC'06, 2007, LNCS volume 4346.
7. L. Brim, J.C. van de Pol, editors, Proceedings of PDMC 2009, *Electronic Proceedings in Theoretical Computer Science* 14, 2009.
8. J.C. van de Pol, and M. Weber, editors, Proceedings of *SPIN Workshop on Model Checking Software*, Twente, 2010, LNCS volume 6349.

10.3 International refereed journals

1. J.C. van de Pol, Operational semantics of rewriting with priorities. *Theoretical Computer Science*, 200(1-2):289–312, 1998.
2. H. Zantema and J.C. van de Pol, A rewriting approach to binary decision diagrams. *Journal of Logic and Algebraic Programming*, 49:61–86, 2001.
3. J.J.M. Hooman and J.C. van de Pol. Semantic models of a timed distributed data-space architecture. *Theoretical Computer Science*, 331(2-3):291–323, 2005.
4. W. Fokkink, J.C. van de Pol, and S. Vijay. Which two-sorted algebras of booleans and naturals have a finite basis? *Algebra Universalis*, 52(4):469–485, 2005.
5. B. Badban, W. Fokkink, J.F. Groote, J. Pang, and J.C. van de Pol. Verification of a sliding window protocol in μ CRL and PVS. *Formal Aspects of Computing*, 17(3):342–388, 2005.

6. B. Badban and J.C. van de Pol. Zero, successor and equality in BDDs. *Annals of Pure and Applied Logic*, 133(1-3):101–123, 2005.
7. S.M. Orzan and J.C. van de Pol, Distribution of a simple shared dataspace architecture. *Fundamenta Informaticae*, 73(4):535–559, 2006.
8. W. Fokkink, J. Pang and J.C. van de Pol, Cones and Foci: A Mechanical Framework for Protocol Verification. *Formal Methods in System Design*, 29:1–31, 2006.
9. M. Valero Espada, J.C. van de Pol, An abstract interpretation toolkit for μ CRL, *Formal Methods in System Design*, 30(3):249–273, 2007
10. B. Badban, J.C. van de Pol, O. Tveretina and H. Zantema, Generalizing DPLL and Satisfiability for Equalities, *Information and Computation* 205(8):1188-1211, 2007.
11. S.C.C. Blom, Thomas Deiß, N. Ioustinova, A. Kontio, J.C. van de Pol, A. Rennoch, and N. Sidorova, Simulated Time for Host-Based Testing with TTCN-3. In: *Software Testing, Verification and Reliability* 18(1):29–49, March 2008.
12. P. Crouzen, J.C. van de Pol and A. Rensink, Applying Formal Methods to Gossiping Networks with mCRL and Groove. In: *ACM SIGMETRICS performance evaluation review* 36(3):7-16, 2008
13. J. Barnat, J. Chaloupka, and J.C. van de Pol, Distributed Algorithms for SCC Decomposition. In: *Journal of Logic and Computation*, Advance Access 17 February 2009.
14. S.C.C. Blom, B. Lisser, J.C. van de Pol and M. Weber, A Database Approach to Distributed State-Space Generation. In: *Journal of Logic and Computation*, Advance Access 05 March 2009.
15. A.J. Wijs, J.C. van de Pol and E.M. Bortnik Solving scheduling problems by untimed model checking. The clinical chemical analyser case study. In: *Journal on Software Tools for Technology Transfer* 11 (5). pp. 375-392, 2009.
16. H.H. Hansen, J. Ketema, S.P. Luttik, M.R. Mousavi and J.C. van de Pol, Towards Model Checking Executable UML Specifications in mCRL2. In: *Innovations in Systems and Software Engineering* 6(1-2):83-90, 2010.

Accepted:

17. J.A. Bergstra and J.C. van de Pol, *A Calculus for Four-Valued Sequential Logic*. In: *Theoretical Computer Science*, 2011
18. L. Aceto, T. Chen, A. Ingólfssdóttir, S.P. Luttik and J.C. van de Pol, *On the Axiomatizability of Priority II*. In: *Theoretical Computer Science*, 2011

10.4 International refereed conference proceedings

1. J.C. van de Pol, *Termination proofs for higher-order rewrite systems*. In: J. Heering, K. Meinke, B. Möller, T. Nipkow (Eds.), Higher-Order Algebra, Logic and Term Rewriting (HOA'93). LNCS 816, Springer, pp. 305–325, 1993.

2. J.C. van de Pol and H. Schwichtenberg, *Strict functionals for termination proofs*. In: M. Dezani-Ciancaglini, G.D. Plotkin (Eds.), *Typed Lambda Calculus and Applications (TLCA'95)*. LNCS 902, Springer, pp. 350–364, 1995.
3. J.C. van de Pol, *Two different strong normalization proofs? – computability versus functionals of finite type* – In: G. Dowek, J. Heering, K. Meinke, B. Möller (Eds.), *Higher-Order Algebra, Logic and Term Rewriting (HOA'95)*. LNCS 1074, Springer, pp. 201–220, 1995.
4. J.F. Groote and J.C. van de Pol, *A bounded retransmission protocol for large data packets – a case study in computer checked algebraic verification* – In: M. Wirsing, M. Nivat (Eds.), *Algebraic Methodology And Software Technology (AMAST'96)*. LNCS 1101, Springer, pp. 536–550, 1996.
5. W.F. Fokkink and J.C. van de Pol, *Simulation as a correct transformation of rewrite systems*. In: I. Prívvara, P. Ružička (Eds.), *Mathematical Foundation of Computer Science (MFCS'97)*. LNCS 1295, Springer, pp. 249–258, 1997.
6. J.C. van de Pol, J.J.M. Hooman and E. de Jong, *Formal requirements specification for command and control systems*. In: *Engineering of Computer-Based Systems (ECBS'98)*, IEEE, pp. 37–44, 1998.
7. J.F. Groote, F. Monin and J.C. van de Pol, *Checking verifications of protocols and distributed systems by computer*. In: D. Sangiorgi, R. de Simone (Eds.), *Conference on Concurrency Theory (CONCUR'98)*, LNCS 1466, Springer, pp. 629–655, 1998.
8. J.C. van de Pol, J.J.M. Hooman and E. de Jong, *Modular formal specification of data and behaviour*. In: K. Araki, A. Galloway, K. Taguchi (Eds.), *Integrated Formal Methods (IFM'99)*, Springer, pp. 109–128, 1999.
9. E. de Jong, J.C. van de Pol and J.J.M. Hooman, *Refinement in requirements specification and analysis: a case study*. In: *Engineering of Computer Based Systems (ECBS'00)*, IEEE, pp. 290–298, 2000.
10. J.C. van de Pol and H. Zantema, *Binary decision diagrams by shared rewriting*. In: M. Nielsen, B. Rován (Eds.), *Mathematical Foundations of Computer Science (MFCS'00)*, LNCS 1893, Springer, pp. 609–618, 2000.
11. J.F. Groote and J.C. van de Pol, *State space reduction using partial τ -confluence*. In: M. Nielsen, B. Rován (Eds.), *Mathematical Foundations of Computer Science (MFCS'00)*, LNCS 1893, Springer, pp. 383–393, 2000.
12. J.F. Groote and J.C. van de Pol, *Equational binary decision diagrams*. In: M. Parigot, A. Voronkov (Eds.), *Logic for Programming and Reasoning (LPAR'00)*, LNAI 1955, Springer, pp. 161–178, 2000.
13. J.C. van de Pol, *Just-in-time: on strategy annotations*. In: B. Gramlich and S. Lucas (Eds.), *Workshop on Reduction Strategies (WRS'01)*. *Electronic Notes in Theoretical Computer Science* 57, Elseviers, 2001
14. S.C.C. Blom, W. Fokkink, J.F. Groote, I. van Langevelde, B. Lissner and J.C. van de Pol, *μ CRL: a toolset for analysing algebraic specifications*. In: G. Berry, H. Comon, A. Finkel (Eds.), *Computer Aided Verification (CAV'01, tool paper)*. LNCS 2102, Springer, pp. 250–254, 2001.

15. J.J.M. Hooman and J.C. van de Pol, *Formal verification of replication on a distributed data space architecture*. In: ACM Symposium on Applied Computing (ACM SAC'02), special track on Coordination Models, Languages and Applications, ACM, 8 pages, 2002.
16. J.C. van de Pol and M. Valero Espada, *Formal specification of JavaSpaces architecture using μCRL* . In: F. Arbab, C.L. Talcott (Eds.), Coordination Models and Languages (COORDINATION'02). LNCS 2315, Springer, pp. 274–290, 2002.
17. J.C. van de Pol, *JITty: a Rewriter with Strategy Annotations*. In: S. Tison (Ed.), Rewriting Techniques and applications (RTA'02, system demo). LNCS 2378, Springer, pp. 367–370, 2002.
18. S.C.C. Blom and J.C. van de Pol, *State Space Reduction by Proving Confluence*. In: E. Brinksma, K.G. Larsen (Eds.), Computer Aided Verification (CAV'02). LNCS 2404, Springer, pp. 596–609, 2002.
19. W. Fokkink, N. Ioustinova, E. Kessler, J.C. van de Pol, Y. Usenko, Y. Yushtein, *Refinement and verification applied to an in-flight data acquisition unit*. In: L. Brim, P. Jancar, M. Kretínský, A. Kucera (Eds.), Conference on Concurrency Theory (CONCUR'02). LNCS 2421, Springer, pp. 1–23, 2002.
20. S.M. Orzan and J.C. van de Pol, *Distribution of a simple shared dataspace architecture*. In: A. Brogi, J.-M. Jacquet (Eds.), Foundations of Coordination Languages and Software Architectures (FOCLASA '02). *Electronic Notes in Theoretical Computer Science* 68(3), Elsevier Science, 2003.
21. J.C. van de Pol and M. Valero Espada, *Verification of JavaSpaces (TM) Parallel Programs*. In: J. Lilius, F. Balarin, R.J. Machado (Eds.), Application of Concurrency to System Design (ACSD'03). IEEE, pp. 196–205, 2003.
22. S.C.C. Blom, J.F. Groote, I.A. van Langevelde, B. Lissner, J.C. van de Pol, *New developments around the μCRL tool set*. In: Formal Methods in Industrial Critical Systems (FMICS'03). *Electronic Notes in Theoretical Computer Science* 80, Elsevier Science, 2003.
23. J.J.M. Hooman and J.C. van de Pol, *Equivalent Semantic Models for a Distributed Dataspace Architecture*. In: F.S. de Boer, M.M. Bonsangue, S. Graf, W.P. de Roever (Eds.), Formal Methods for Components and Objects (FMCO'02). LNCS 2852, Springer, pp. 182–201, 2003.
24. S.M. Orzan and J.C. van de Pol, *Verification of distributed dataspace architectures*. In: M. Broy, A.V. Zamulin (Eds.), Perspectives of System Informatics (PSI'03). LNCS 2890, Springer, 2003.
25. J.C. van de Pol and M. Valero Espada, *Modal abstractions in μCRL* . In: C. Rattray, S. Maharaj, and C. Shankland, editors, Algebraic Methodology And Software Technology (AMAST'04). LNCS 3116, Springer, pp. 409–425, 2004
26. B. Badban, W.J. Fokkink, J.F. Groote, J. Pang, and J.C. van de Pol, *Verifying a sliding window protocol in μCRL* . In: C. Rattray, S. Maharaj, and C. Shankland, editors, Algebraic Methodology And Software Technology (AMAST'04). LNCS 3116, Springer, pp. 148–163, 2004.

27. J. Pang, J.C. van de Pol, and M. Valero Espada, *Abstraction of parallel uniform processes with data*. In: Software Engineering and Formal Methods (SEFM'04). IEEE Computer Society, Beijing, China, 2004.
28. S.M. Orzan, J.C. van de Pol, and M.A. Valero Espada, *A state space distribution policy based on abstract interpretation*. In: Parallel and Distributed Methods for verification (PDMC'04). *Electronic Notes in Theoretical Computer Science*, 128(3), pp. 35–45, 2005.
29. J.C. van de Pol and M.A. Valero Espada, *An abstract interpretation toolkit for μ CRL*. In: Formal Methods in Industrial Critical Systems (FMICS'04). *Electronic Notes in Theoretical Computer Science*, 133. pp. 295–313, 2005.
30. J.R. Calamé, N. Ioustinova, J.C. van de Pol, and N. Sidorova, *Data Abstraction and Constraint Solving for Conformance Testing*. In: Asia-Pacific Software Engineering Conference (APSEC'05), IEEE press, pp. 541–548, 2005.
31. J.C. van de Pol and O. Tveretina, *A BDD-representation for the logic of equality and uninterpreted functions*. In: J. Jedrzejowicz, A. Szepietowski (Eds.), Mathematical Foundations in Computer Science (MFCS'05). LNCS 3618, Springer, pp. 769–780, 2005.
32. J.C. van de Pol and H. Zantema, *Generalized innermost rewriting*. In: Jürgen Giesl, editor, Rewriting Techniques and Applications (RTA'05). LNCS 3467, Springer, pp. 2-16, 2005.
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